



**REPUBLIC** 



# **DEMOCRITUS UNIVERSITY OF THRACE**

# SCHOOL OF ENGINEERING

# DEPARTMENT OF ELECTRICAL AND COMPUTER ENGINEERING

Building A' E&CE, University Campus-Kimmeria 67100 Xanthi, Greece

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www.ee.duth.gr

# **DIPLOMA SUPPLEMENT**

# (Valid with diploma No 3192)

This Diploma Supplement follows the model developed by the European Commission, Council of Europe and UNESCO/CEPES. The purpose of the supplement is to provide sufficient independent data to improve the international 'transparency' and fair academic and professional recognition of qualifications (diplomas, degrees, certificates etc.). It is designed to provide a description of the nature, level, context, content and status of the studies that were pursued and successfully completed by the individual named on the original qualification to which this supplement is appended. It should be free from any value judgements, equivalence statements or suggestions about recognition. Information in all eight sections should be provided. Where information is not provided, an explanation should give the reason why.

# **1. INFORMATION IDENTIFYING THE HOLDER OF THE QUALIFICATION**

- **1.1 Family Name(s): CHATZIPAPAS**
- 1.2 Given name(s): NIKOLAOS
- 1.3 Date of birth (day/month/year): 06/06/1998 Place, Country of Birth: THESSALONIKI, GREECE
- 1.4 Student identification number or code (if available): 57370

# 2. INFORMATION IDENTIFYING THE QUALIFICATION

- **2.1** Name of qualification and (if applicable) title conferred (in original language): ΔΙΠΛΩΜΑ ΗΛΕΚΤΡΟΛΟΓΩΝ ΜΗΧΑΝΙΚΩΝ ΚΑΙ ΜΗΧΑΝΙΚΩΝ ΥΠΟΛΟΓΙΣΤΩΝ transliterated into: DIPLOMA ILEKTROLOGON MICHANIKON KAI MICHANIKON YPOLOGISTON, in English: DIPLOMA IN ELECTRICAL AND COMPUTER ENGINEERING
- **2.2 Main field(s) of study for the qualification:** ELECTRICAL AND COMPUTER ENGINEERING
- **2.3** Name and status of awarding institution (in original language): ΔΗΜΟΚΡΙΤΕΙΟ ΠΑΝΕΠΙΣΤΗΜΙΟ ΘΡΑΚΗΣ, transliterated into: Dimokriteio Panepistimio Thrakis - in English: Democritus University of Thrace, D.U.Th., Public University
- 2.4 Name and status of institution (if different from 2.3) administering studies (in original language): As in 2.3
- AS IN 2.32.5 Language(s) of instruction/examination: GREEK

# 3. INFORMATION ON THE LEVEL OF THE QUALIFICATION

- 3.1 Level of qualification: Integrated Master
- **3.2 Official length of programme:** Duration of studies: 5 years / 10 semesters ECTS credits: 300 ECTS
- **3.3** Access requirement(s):

Apolytirio (Certificate) from Lykeion (High School - Secondary Education) and Pan-Hellenic examinations, by way of special categories or by assessment examinations.

## 4. INFORMATION ON THE CONTENTS AND RESULTS GAINED

# 4.1 Mode of study:

Full-time

## 4.2 **Programme requirements:**

According to the Undergraduate Studies Internal Regulations of the Electrical and Computer Engineering - ECE Department, students obtain a diploma after attending and passing successfully in all the courses of their program of studies and having completed and defended successfully their diploma thesis, accumulating in this way 300 ECTS credits. More analytically: The duration of studies that lead to the diploma in Electrical and Computer Engineering (ECE) is 5 years. The academic year begins in September 1st of every year and ends in August 31st of the next year. The educational period of each academic year is divided in 2 semesters, the Fall and the Spring, each having a 13-week teaching period and a 3-week examination period.

The courses are distributed in 9 instructive semesters, while the last, 10th semester is dedicated in preparing the diploma thesis. The program of studies of the first 6 semesters is common for all students and is referred to as Core Course Program or Fundamental Studies. Beginning in the 7th semester, students select the Specialized Direction of Study that they will follow. The "Directions of Study" are Groups of Elective Courses that exhibit scientific relevance and orientate the knowledge of the graduate Engineer toward one of the basic ECE Directions. It is noted that the granted "Electrical and Computer Engineer" Diploma is a uniform one, and the mandatory basic courses, that are common for all graduates, secure the necessary knowledge to grant this uniform Diploma. At the ECE Department of D.U.TH., there exist three Specialized Directions of Study: a) Energy Electrical Engineering, b) Electronics and Computer Engineering, and c) Telecommunications Engineering. During the 9th semester, depending on the Direction of studies, students elect all the courses they wish to attend following one of the possible offered Directions. In the beginning of the 9th semester, students also select the Diploma Thesis and the successful completion of the Diploma Thesis is mandatory for graduation. In addition, students may elect an optional Practical Training in centers oriented/specialising in consultancy, research or projects and in the industry in lieu of elective courses.

The prerequisites to obtain the ECE Diploma are:

To attend and obtain a passing grade in 65 semester courses: 51 mandatory and 14 elective courses. The final grade of each course is based in the performance of the individual student in various activities, such as mid-term and final examinations, homework, and laboratory reports, depending on the course. All courses are characterized by the number of credit units (Credit Units, C.U.), which are calculated by adding the hours of taught course, tutorials, and half of the hours of laboratory work. The mandatory courses are assigned with 5 or 4 ECTS credits, and the elective courses with 3 ECTS credits.

Completion, writing and successful defense of the Diploma Thesis, which is equivalent to an entire semester of taught courses and provides 30 ECTS credits.

Upon completion of his studies, the graduate of the Department of Electrical and Computer Engineering has acquired the knowledge necessary to practice the profession of Electrical and Computer Engineering or continue studying for a postgraduate diploma.

Specifically, graduates of the Department of Electrical and Computer Engineering upon successful completion of the study program, in addition to the basic knowledge of all fields of the science of Electrical and Computer Engineering and the ability to exercise their profession, have also been trained to have the ability to apply their knowledge successfully in practice, search, analyze and synthesize data and information using the appropriate and modern technologies, adapt to novel situations, to keep abreast of developments and to make decisions, work independently or participating in groups at the local, international and interdisciplinary environment, design and manage projects of various size and complexity generate new ideas in research and promote the free and creative thinking.

In addition to the above competence, which arise from the content and learning outcomes of the core compulsory courses of the study program, each graduate acquires additional specialized competence from the content and learning outcomes of the course of the "Direction of Study" chosen by and from the subject of his diploma thesis. These specialized competence, come from one of the three main "Directions of Study" offered in the Department, namely, (1) Energy Electrical Engineering, (2) Electronics and Computer Engineering and (3) Telecommunications Electrical Engineering,

Increase the general skills of graduates guaranteeing particular scientific knowledge in specific areas of science and Electrical and Computer Engineering Maximize the opportunities for independent and lifelong learning to ensure continuous updating and acquiring of modern knowledge.

Additional information can be found in the web page of the Department, http//www.ee.duth.gr and the Guides of Undergraduate and Post graduate Studies.

# 4.3 Programme details (e.g. modules or units studied) and the individual grades/marks/credits obtained:

The courses in which the above mentioned student has been examined and got passing grades, as well as courses for which he/she has received recognition, are the following (C=Compulsory, O=Optional, OD,O1,O2=Optional Direction, CS=Compulsory Selective, CD=Compulsory Direction, S=Specialization):

Course Code	Course Title	Course Type	Grade	Examination period	Examination Type	ECTS
5A1Y03	APPLIED THERMODYNAMICS	С	6.5	JUNE 2017-2018	Examination	4
5A1YE	*ENGLISH LANGUAGE I	0	7.5	FEB 2017-2018	Examination	3
5A3Y	INTRODUCTION TO COMPUTER SCIENCE	С	6	FEB 2017-2018	Examination	4
5A4YE	SCIENCE PHILOSOPHY	CS	10	FEB 2017-2018	Examination	3
5B1Y	PHYSICS I	С	10	FEB 2017-2018	Examination	5
5A5YN	TECHNICAL DRAWING	С	6.5	FEB 2017-2018	Examination	4
5A2YN	CALCULUS OF ONE VARIABLE- LINEAR ALGEBRA	С	7.5	FEB 2017-2018	Examination	5
5AZ7YN	DISCRETE MATHEMATICS	С	9	SEPT 2017-2018	Examination	5
ΣΤ8Ε	ENGINEERING SYSTEMS SAFETY	CS	9	JUNE 2018-2019	Examination	3
5B2Y	CALCULUS OF SEVERAL VARIABLES	С	10	JUNE 2017-2018	Examination	5
5B3Y	DIFFERENTIAL EQUATIONS	С	7	JUNE 2017-2018	Examination	4
5B5Y	STRUCTURED PROGRAMMING	С	5	FEB 2017-2018	Examination	5
5B7Y	ELECTRIC MEASUREMENTS	С	7	FEB 2017-2018	Examination	5
5Γ1Υ	PHYSICS II	С	8	SEPT 2017-2018	Examination	4
5Δ6Y	MATERIALS SCIENCE	С	8.5	JUNE 2017-2018	Examination	4
5∆5Y	DESIGN OF ELECTRONIC CIRCUITS	С	7.5	FEB 2017-2018	Examination	5
5Г2Ү	APPLIED NUMERICAL ANALYSIS	С	6.5	SEPT 2017-2018	Examination	4
5Г3Е	COMPLEX FUNCTIONS AND TRANSFORMS	С	9	FEB 2017-2018	Examination	4
5Г4ҮЕ	PARTIAL DIFFERENTIAL EQUATIONS	CS	8	FEB 2017-2018	Examination	3
5Г5Ү	ELECTROMAGNETIC FIELDS I	С	8	SEPT 2018-2019	Examination	5
5Г6Ү	ELECTRIC CIRCUITS I	С	9	SEPT 2017-2018	Examination	5
5Г9YN	OBJECT ORIENTED PROGRAMMING	С	10	SEPT 2017-2018	Examination	4
5Δ10YN	MICROELECTRONICS	С	7.5	JUNE 2017-2018	Examination	4
5Δ2Υ	THEORY OF PROBABILITY AND STATISTICS	С	9.5	JUNE 2017-2018	Examination	4
5Δ3Y	ELECTRIC CIRCUITS II	С	8	SEPT 2017-2018	Examination	5

Course Code	Course Title	Course Type	Grade	Examination period	Examination Type	ECTS
5Δ4Y	ELECTROMAGNETIC FIELDS II	С	6.5	SEPT 2019-2020	Examination	5
5Γ4Y	TECHNICAL MECHANICS	С	8.5	JUNE 2017-2018	Examination	4
5 <b>Δ</b> 8ΥΕ	QUEUEING THEORY	CS	10	JUNE 2017-2018	Examination	3
5E2Y	DESIGN OF DIGITAL SYSTEMS	С	7	SEPT 2017-2018	Examination	5
5E3Y	SIGNALS AND SYSTEMS	С	9.5	FEB 2018-2019	Examination	4
5E12Y	BASIC PRINCIPLES OF ELECTRICAL MACHINES	С	9.5	FEB 2018-2019	Examination	5
5E15Y	APPLIED ELECTRODYNAMICS	С	6.5	FEB 2020-2021	Examination	4
5E16Y	STRUCTURE AND OPERATION OF POWER SYSTEMS	С	8.5	FEB 2018-2019	Examination	5
5E17Y	ALGORITHMS AND DATA STRUCTURES	С	8.5	FEB 2018-2019	Examination	4
5ΣΤ1ΗΚ	SOUND TECHNOLOGY	CS	8	FEB 2018-2019	Examination	3
5ΣT2	ANALOGUE ELECTRONIC CIRCUITS	С	7.5	FEB 2018-2019	Examination	5
5ΣΤ3	BASIC PRINCIPLES OF POWER ELECTRONICS	С	10	SEPT 2018-2019	Examination	5
5 <b>Σ</b> Τ26ΥΕ	SENSORS	CS	7.5	JUNE 2018-2019	Examination	3
5ΣT22Y	PRINCIPLES OF COMMUNICATION LINKS	С	8.5	SEPT 2018-2019	Examination	5
5ΣT24Y	ORGANIZATION OF COMPUTER SYSTEMS	С	9	JUNE 2018-2019	Examination	4
5E6Y	AUTOMATIC CONTROL SYSTEMS	С	8	JUNE 2018-2019	Examination	4
5Z4	PRINCIPLES OF COMMUNICATIONS SYSTEMS	С	9.5	JUNE 2019-2020	Examination	5
5H14	CONSUMERS ELECTRICAL NETWORKS	С	7.5	SEPT 2019-2020	Examination	4
5H2	INTERGRATED CIRCUITS	CD	9.5	FEB 2019-2020	Examination	4
5H1	OPERATING SYSTEMS	01	10	FEB 2020-2021	Examination	3
5E1Y	ELECTRICAL MACHINES I	02	9	FEB 2019-2020	Examination	3
5Z1	NETWORKS	CD	9.5	FEB 2019-2020	Examination	4
5Z2	MICROPROCESSORS AND APPLICATIONS	CD	7	SEPT 2019-2020	Examination	4
5Z31Y	MICROELECTRONICS TECHNOLOGY AND DEVICES	CD	9.5	FEB 2019-2020	Examination	4
5ΣΤ5	MODERN AUTOMATIC CONTROL	CD	10	FEB 2019-2020	Examination	4
5H5	DIGITAL COMMUNICATIONS SYSTEMS	CD	7	FEB 2019-2020	Examination	4

Course Code	Course Title	Course Type	Grade	Examination period	Examination Type	ECTS
5ΣΤ1	ELECTRICAL MACHINES II	02	8	JUNE 2019-2020	Examination	3
5H9	COMPUTATIONAL INTELLIGENCE	CD	10	JUNE 2019-2020	Examination	5
5036YN	ANALYSIS AND SYNTHESIS OF COMPLEX ELECTRONIC SYSTEMS	01	10	JUNE 2019-2020	Examination	3
5H69Y	ELECTRONIC MEASUREMENTS	CD	9	JUNE 2019-2020	Examination	4
5H68Y	VLSI SYSTEMS	CD	10	JUNE 2019-2020	Examination	5
5H38	COMPUTER NETWORKS	CD	8	JUNE 2019-2020	Examination	5
5H4	DIGITAL SIGNAL PROCESSING	CD	8	JUNE 2019-2020	Examination	5
5H37	PATTERN RECOGNITION	01	9	FEB 2020-2021	Examination	3
05 <del>0</del> 10	ROBOTICS	CD	8	FEB 2020-2021	Examination	5
05Θ12	SPECIAL ISSUES OF AUTOMATIC CONTROL SYSTEMS	01	10	FEB 2020-2021	Examination	3
05022	BIOMEDICAL TECHNOLOGY	02	10	FEB 2020-2021	Examination	3
5 <del>0</del> 34	EMBEDDED SYSTEMS DESIGN	CD	9	FEB 2020-2021	Examination	5
5 <del>0</del> 38	COMPUTER SYSTEMS SECURITY	01	10	FEB 2020-2021	Examination	3
5 <del>0</del> 55	QUANTUM COMPUTERS	CD	6.5	JUNE 2020-2021	Examination	5
5 <del>0</del> 10E	HIGH LEVEL SYNTHESIS FOR INTEGRATED CIRCUITS DESIGN	01	9.5	FEB 2020-2021	Examination	3

# **Courses ECTS Total (1)**

270

The column of ECTS presents the credits that correspond to each course.

Exemption is defined as recognition applied when a student carries the ECTS credits from previously completed and recognized studies, e.g. Erasmus.

\*Does not count towards diploma.

Diploma Thesis Grade: 10

Thesis ECTS (2):

# ECTS Total (1)+(2):

300

# 4.4 Grading scheme and, if available, grade distribution guidance:

According to the Institution's Internal Regulations, the grading system falls into the 0-10 scale as follows:

30

8.50 - 10	: "Άριστα (Arista) - Excellent"
7.00 - 8.49	: "Λίαν Καλώς (Lian Kalos) - Very Good"
6.00 - 6.99	: "Καλώς (Kalos) - Good"

5.00 - 5.99 : "Επαρκώς (Eparkos) - Fair"

0.00 - 4.99 : "Ανεπιτυχώς (Anepitichos) - Fail"

At least a grade of 5.0 is required for the successful completion of a course. The diploma grade (D.G.) is calculated with accuracy to two decimal places by the following expression: D.G. = (5/6)x(Summation of all grades of courses/Number of courses)+(1/6)x(grade of Diploma

D.G. = (5/6)x(Summation of all grades of courses/Number of courses)+(1/6)x(grade of Diploma thesis)

**4.5 Overall classification of the qualification:** "EXCELLENT" 8,71 EIGHT AND SEVENTY-ONE HUNDREDTHS

# 5. INFORMATION ON THE FUNCTION OF THE QUALIFICATION

# 5.1 Access to further study:

Access to Post Graduate studies

# 5.2 **Professional status (if applicable):**

The Diploma degree in Electrical and Computer Engineering (ECE) discipline entitles its holder to the legally protected professional title of "Engineer" and certifies his/her knowledge and rights to exercise professional work in the field of "Systems Study and manufacture for generation, transmission, distribution. storage, processing, control and utilisation of energy and information". Graduates of the ECE Department are licensed to exercise the profession of Mechanical-Electrical Engineering by the Technical Chamber of Greece, after passing examinations, and the corresponding professional rights according to the existing legislation of the state.

# 6. ADDITIONAL INFORMATION

# 6.1 Additional information:

# **6.2** Further information sources:

- o Website of the Department ECE: <http://www.ee.duth.gr>
- o Website of Democritus University of Thrace: <http://www.duth.gr>

o Website of Ministry of Education, Research and Religious Affairs: <a href="http://www.minedu.gov.gr">http://www.minedu.gov.gr</a>

# 7. CERTIFICATION OF THE SUPPLEMENT

# 7.1 Date: 29/12/2021

7.2 Name and Signature:

By Rector's Order Pavlos Pavlidis

(signature)

## Deputy Secretary of the DEPARTMENT OF ELECTRICAL AND COMPUTER ENGINEERING

# 7.4 Official stamp or seal:

**Capacity:** 

7.3

# 8. INFORMATION ON THE NATIONAL HIGHER EDUCATION SYSTEM

#### (i) Structure:

According to the Framework Law (2007), higher education consists of two parallel sectors: the University sector (Universities, Polytechnics, Fine Art Schools, the Open University) and the Technological sector (Technological Education Institutions (TEI) and the School of Pedagogic and Technological Education). The same law regulates issues concerning governance of higher education along the general lines of increased participation, greater transparency, accountability and increased autonomy.

There are also state non-university tertiary Institutions offering vocationally oriented courses of shorter duration (two to three years) which operate under the authority of other ministries.

#### (ii) Access

Entrance to the various Schools of the universities (Panepistimio) and technological education institutions (Techonoligo Ekpaideftiko Idryma - TEI) depends on the general score obtained by Lyceum graduates on the certificate, as described above (Section 5.iv), on the number of available places (numerous clausus) and on the candidates' ranked preferences among schools and sections.

#### (iii) Qualifications

Students who successfully complete their studies at Universities and TEI are awarded a Ptychio (fist cycle degree). First cycle programmes last four years for most fields to five years for engineering and certain other applied science fields and six years for medicine. The Ptychio leads to employment or further study at the post-graduate level that includes the one year second cycle leading to the second degree, Metaptychiako Dimpoma Eidikefsis - equivalent to the Master's degree - and the third cycle leading to the doctorate degree, Didactoriko Diploma.

Recent legislation on quality assurance in higher education, the credit transfer system and the diploma supplement defines the framework and criteria for evaluation of university departments and for certification of student degrees. These measures aim at promoting student mobility and contributing to the creation of a European Higher Education Area.

A detailed description of the Greek Education System is offered in the EURYDICE http://www.eurydice.org database of the European Education Systems.

http://eacea.ec.europa.eu/education/eurydice/documents/thematic\_reports/122EN.pdf (pages 82,83)

http://www.eurydice.org

